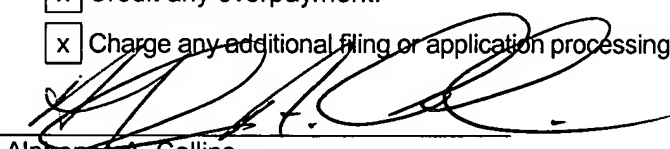


09-22-03

2816

AMENDMENT TRANSMITTAL LETTER				Docket No. 1298/1F986US2	
Application No. 10/033,525-Conf. #7823		Filing Date December 28, 2001		Examiner Not Yet Assigned	
				Art Unit N/A	
Applicant(s): Rajasekhar Pullela, et al.					
Invention: TRANS-ADMITTANCE TRANS-IMPEDENCE LOGIC FOR INTEGRATED CIRCUITS					
TO THE COMMISSIONER FOR PATENTS					
Transmitted herewith is an amendment in the above-identified application.					
The fee has been calculated and is transmitted as shown below.					
CLAIMS AS AMENDED					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	16	- 20 =		x	0.00
Independent Claims	3	- 3 =		x	0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify):					
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:					0.00
<input checked="" type="checkbox"/> Large Entity <input type="checkbox"/> Small Entity					
<input checked="" type="checkbox"/> No additional fee is required for this amendment.					
<input type="checkbox"/> Please charge Deposit Account No. _____ in the amount of \$ _____ A duplicate copy of this sheet is enclosed.					
<input type="checkbox"/> A check in the amount of \$ _____ to cover the filing fee is enclosed.					
<input type="checkbox"/> Payment by credit card. Form PTO-2038 is attached.					
<input checked="" type="checkbox"/> The Director is hereby authorized to charge and credit Deposit Account No. <u>04-01000</u> as described below. A duplicate copy of this sheet is enclosed.					
<input checked="" type="checkbox"/> Credit any overpayment.					
<input checked="" type="checkbox"/> Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.					
				Dated: <u>September 18, 2003</u>	
Alphonso A. Collins Attorney Reg. No.: 43,559					
DARBY & DARBY P.C. P.O. Box 5257 New York, New York 10150-5257 (212) 527-7717					

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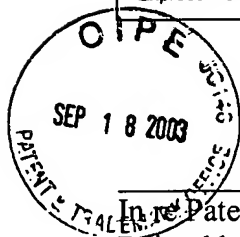
EV 306627128WS

Express Mail Label No.	Dated: <u>9/18/03</u>
------------------------	-----------------------

12/B

EV306627128 US 9/18/03
Express Mail Label No. Dated:

Docket No.: 1298/1F986US2
(PATENT)



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Rajasekhar Pullela, et al.

Application No.: 10/033,525

Art Unit: 2816

Filed: December 28, 2001

Examiner: LE, Dinh Tranh

For: TRANS-ADMITTANCE TRANS-IMPEDENCE
LOGIC FOR INTEGRATED CIRCUITS

10/1/03
Adm 1h

AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION

MS Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

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INTRODUCTORY COMMENTS

In response to the Office Action dated August 22, 2003, please amend the above-identified U.S. patent application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begin on page 3 of this paper.

Remarks/Arguments begin on page 7 of this paper.